



FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (Rev. 7-80) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary) DATE MAILED: 11/21/02	ATTY. DOCKET NO. 990530	SERIAL NO. 09/981,130
	APPLICANT Patel, et al.	
	FILING DATE October 17, 2001	GROUP 2121-224

U.S. PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPRO- PRIATE
	A1						
	A2						
	A3						
	A4						
	A5						

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	Ref No	DOCUMENT NUMBER	DATE	COUNTRY	NAME	CLASS	SUB CLASS
	B1						
	B2						

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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Page, Etc.)

CD	C1	Parhami, Behrooz: "Analysis of Tabular Methods for Modular Reduction" Signals, Systems and Computers, 1994. USA, IEEE COMPUT. SOC. US. pgs. 526-530 (1994).
	C2	
EXAMINER	DATE CONSIDERED 8/30/04	
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.		

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